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Docket No.: 1614.1082
62-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hideo MIYAKE et al.

Serial No. 09/671,117

Group Art Unit: 2186

Confirmation No. 8617

Filed: September 28, 2000

Examiner: Hong Chong Kim

For: METHOD OF CONTROLLING A CACHE MEMORY TO INCREASE AN ACCESS SPEED TO A MAIN MEMORY, AND A COMPUTER USING THE METHOD

AMENDMENT

RECEIVED

JUN 02 2003

Assistant Commissioner for Patents
Washington, D.C. 20231

Technology Center 2100

Sir:

This is in response to the Office Action mailed January 2, 2003, and having a period for response set to expire on April 2, 2003. A Petition for a two-month extension of time, together with the requisite fee for same, is submitted herewith, thereby extending the period for response to June 2, 2003. The following amendments and remarks are respectfully submitted.

IN THE CLAIMS:

Please CANCEL claims 1-12, 18, and 19.

Please AMEND claims 13 and 17 as follows:

05/30/2003 HMEKONEN 00000007 09671117

01 FC:1252

410.00 OP

DAB

13. (TWICE AMENDED) A method of controlling a cache memory that is connected to a main memory with a first address space and capable of acting as a random access memory, which is executed by a computer that accesses the main memory through the cache memory, comprising: